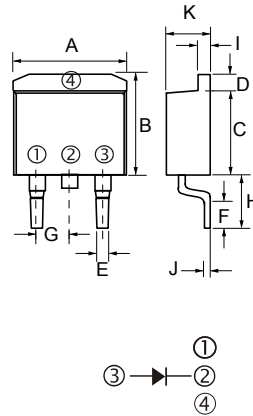


Features

- Schottky Barrier Chip
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V-O

TO-263AC/ D²PAK



| TO-263AC/ D ² PAK | | |
|------------------------------|------|-------|
| DIM. | MIN. | MAX. |
| A | 9.80 | 10.20 |
| B | 9.60 | 10.60 |
| C | 8.50 | 9.20 |
| D | ---- | 1.67 |
| E | 0.51 | 1.01 |
| F | 2.10 | 2.50 |
| G | 2.44 | 2.64 |
| H | 4.40 | 4.70 |
| I | 1.10 | 1.40 |
| J | 0.30 | 0.64 |
| K | 4.40 | 4.80 |
| All Dimensions in millimeter | | |

Mechanical Data

- Case: TO-263AC(D²PAK), Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 2.24 grams (approx.)
- Mounting Position: Any
- **Lead Free: For RoHS / Lead Free Version**

Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

| Characteristic | Symbol | MBR 640G | MBR 645G | MBR 650G | MBR 660G | MBR 680G | MBR 6100G | MBR 6150G | MBR 6200G | Units |
|---|-----------------------------------|-------------|----------|----------|----------|----------|-----------|-----------|-----------|-------|
| Peak Repetitive Reverse Voltage | V _{RRM} | 40 | 45 | 50 | 60 | 80 | 100 | 150 | 200 | V |
| Working Peak Reverse Voltage | V _{RWM} | | | | | | | | | |
| DC Blocking Voltage | V _R | | | | | | | | | |
| RMS Reverse Voltage | V _{R(RMS)} | 28 | 31 | 35 | 42 | 56 | 70 | 105 | 140 | V |
| Average Rectified Output Current @T _L = 100°C (Note 1) | I _O | 6.0 | | | | | | | | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method) | I _{FSM} | 100 | | | | | | | | A |
| Forward Voltage @I _F = 6A | V _{FM} | 0.70 | | 0.80 | | 0.85 | | 0.92 | | V |
| Peak Reverse Current @T _A = 25°C At Rated DC Blocking Voltage @T _A = 100°C | I _{RM} | 0.1 | | | | | | | | mA |
| | | 20 | | | | | | | | |
| Typical Junction Capacitance (Note 2) | C _j | 350 | | 280 | | 200 | | | | pF |
| Typical Thermal Resistance (Note 1) | R _{θJA} | 60 | | | | | | | | °C/W |
| Operating and Storage Temperature Range | T _j , T _{STG} | -55 to +150 | | | | | | | | °C |

Note: 1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

FIG.1- FORWARD CURRENT DERATING CURVE

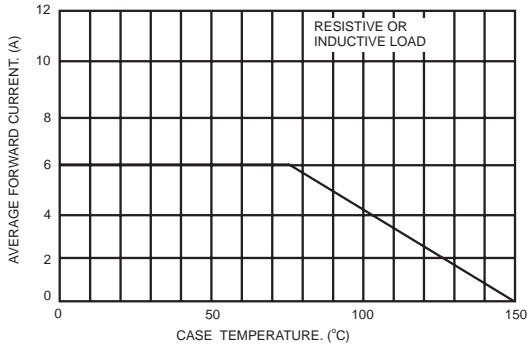


FIG.2- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

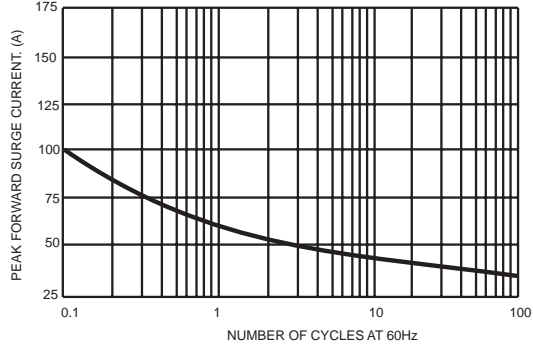


FIG.3- TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

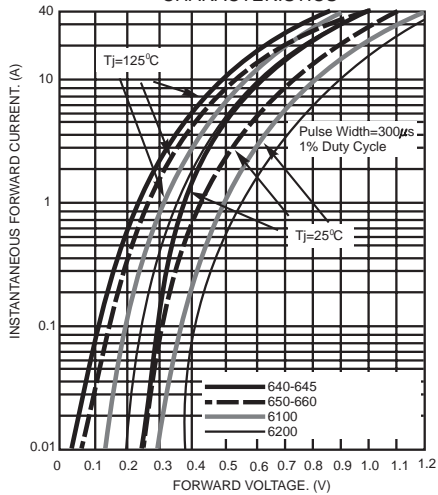


FIG.4- TYPICAL REVERSE CHARACTERISTICS

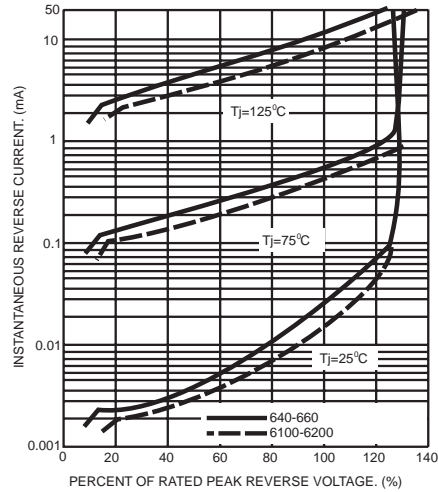


FIG.5- TYPICAL JUNCTION CAPACITANCE

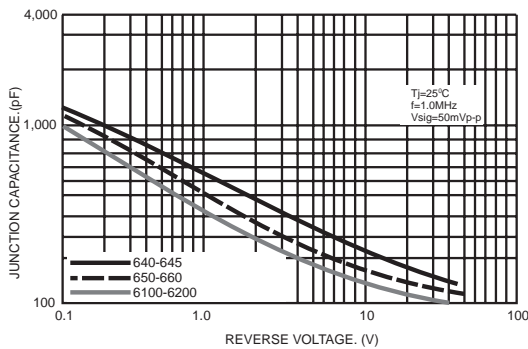


FIG.6- TYPICAL TRANSIENT THERMAL CHARACTERISTIC

